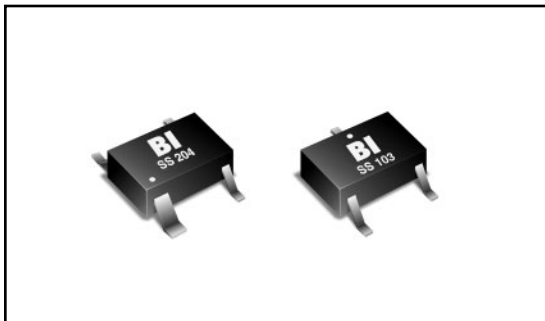


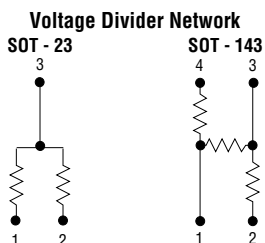
NICHROME ON SILICON

Voltage Divider Circuit Thin Film Resistor Networks

NEW PRODUCT



SCHEMATICS



4

ELECTRICAL

Standard Resistance Range, Ohms	1K to 20K
Operating Temperature Range	-55°C to +125°C
Interlead Capacitance	<2pF
Insulation Resistance	≥10,000 Megohms
Maximum Operating Voltage	100Vdc or √PR
Noise, Maximum (MIL-STD-202, Method 308)	-25dB

ENVIRONMENTAL

Thermal Shock plus Power Conditioning, Maximum	ΔR 0.1%
Short Time Overload, Maximum	ΔR 0.1%
Terminal Strength	ΔR 0.1%
Moisture Resistance, Maximum	ΔR 0.1%
Mechanical Shock, Maximum	ΔR 0.1%
Vibration, Maximum	ΔR 0.1%
Low Temperature Operation	ΔR 0.1%
High Temperature Exposure, Maximum	ΔR 0.1%
Resistance to Solder Heat, Maximum	ΔR 0.05%
Marking Permanency	per MIL-STD-202, Method 215
Storage Temperature Range	-55°C to +125°C

Specifications subject to change without notice.

MECHANICAL

Lead Plating	80/20 Tin Lead
Lead Material	Copper Alloy
Lead Configuration	Gull Wing
Lead Coplanarity	0.004" (0.102mm)
Substrate Material	Silicon
Resistor Material	Passivated Nichrome
Body Material	Molded Epoxy

TOLERANCES

Accuracy Code	B	D	F	G	J
Absolute Resistance Tolerances at 25°C	0.1%	0.5%	1.0%	2.0%	5.0%
Ratio Tolerances at 25°C	0.1%	0.1%	1.0%	N/A	N/A
Temperature Coefficient of Resistance (TCR Code)					±25ppm/°C (Q) ±50ppm/°C (P) ±100ppm/°C (S)
Temperature Coefficient of Resistance, Tracking					±5ppm/°C

PACKAGE POWER, WATTS @ 70°C, MAX.

SOT - 23	SOT - 143
0.20	0.25

Power per resistor @ 70°C, Max. is 100mW, not to exceed package power.

POWER DERATING CURVE

