

MODEL BB1020DT

Low Voltage Differential (LVD) SCSI Termination Network



DESCRIPTION

The BB1020DT is an LVD termination network designed to terminate high performance SPI-2 (Ultra2) and SPI-3 (Ultra3) bus applications. Wide SCSI bus applications can be terminated with three BB1020DT networks and a linear regulator IC.

For use in high-speed SCSI bus applications, the BB1020DT utilizes thick film resistors on a ceramic substrate with ball grid array (BGA) terminals. Resistors and termination balls reside on the same side of the ceramic substrate to yield the absolute minimum stray capacitance and inductance.

FEATURES AND BENEFITS

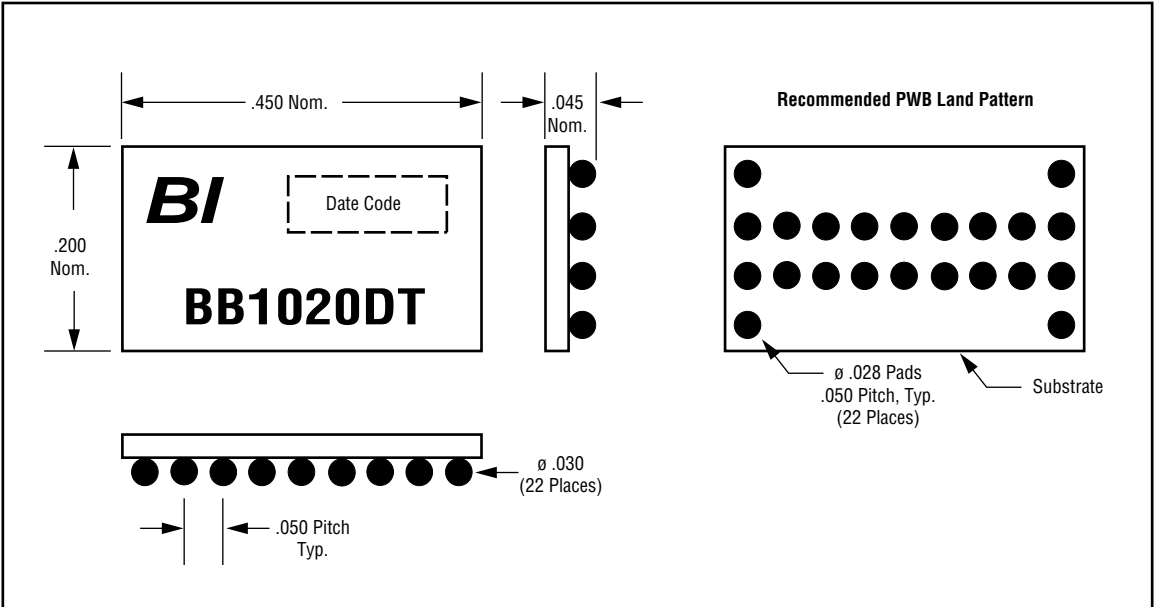
- SPI-2 (Ultra2) and SPI-3 (Ultra3) compliant
- Single network containing 9-line LVD termination
- BGA termination is constructed entirely with high temperature solder
- BGA termination and resistors on the same side offer minimal stray capacitance and inductance
- Compatible with pick and place automation equipment

ELECTRICAL

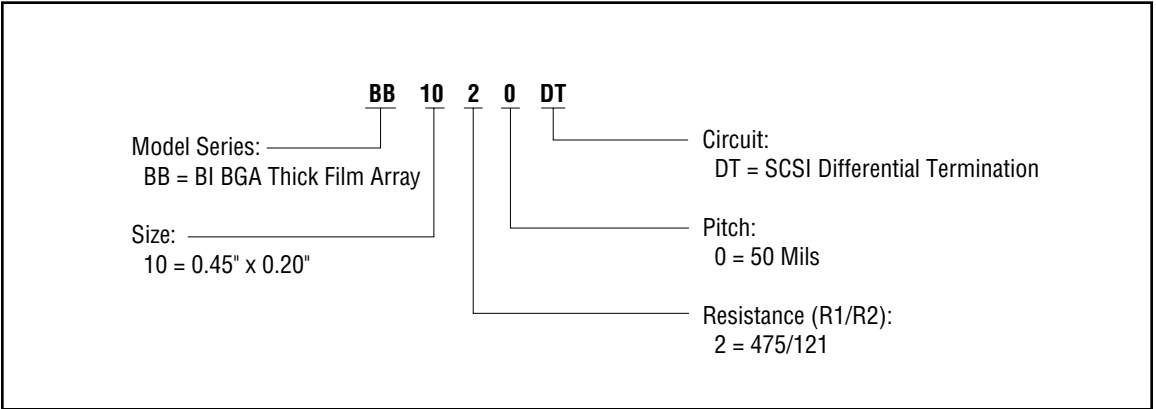
Parameter	Limit	Units
Resistance, Nominal (R1/R2)	475/121	Ω
Absolute Tolerance, Maximum	1	%
Ratio Tolerance, Maximum	1	%
Temperature Coefficient of Resistance, Maximum	100	ppm/ $^{\circ}\text{C}$
Interlead Capacitance, Maximum	0.1	pF
Operating Temperature	0 to +70	$^{\circ}\text{C}$

Specifications subject to change without notice.
Last revision: 04/11/02.

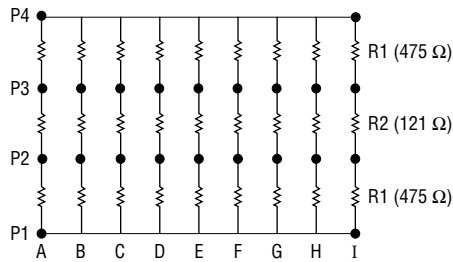
OUTLINE DIMENSIONS (Inch)



ORDERING INFORMATION



SCHEMATIC



TYPICAL APPLICATION

